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APPLICANTS

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** CONTINUING DATA ***** *NONE PML*

** FOREIGN APPLICATIONS *****

JAPAN JP2002-218045 07/26/2002 *YES PML*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/03/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>	JAPAN	5	16	2

ADDRESS

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TITLE

Circuit for detecting abnormal operation of memory and integrated circuit and method for detecting abnormal operation

FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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